AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on page 7, line 14 as follows:

The absolute value circuit 81 obtains an absolute value of the inter-field pixel level difference Sp, which is calculated by the subtractor 6 ~~6a~~ and corresponds to a difference in level between pixels of the input interlaced signal Vin and the 1-field delay input interlaced signal Vd1, and outputs an inter-field pixel level difference absolute value SpA.